



<p><b>LIST OF ART CITED BY APPLICANT</b> <b>(PTO-1449)</b></p>		ATTY. DOCKET NO. YHK-0107	APPLN. SERIAL NO. 10/614,166
		APPLICANT(S) Won Tae Kim and Soo Seok SIM	
		FILING DATE July 8, 2003	GROUP 2821
		<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Publisher, Place of Publication, Etc.)</b>	
<p>*de ZWART et al.; "Wall Voltage Fingerprint Method in Three-Electrode PDP Cell"; December 2001.</p>			
<p>*KIM et al.; "Influence of Ramped Erase Pulse Slope in Reset Period on the Addressing Characteristics in AC-PDP"; December 2001.</p>			
<p>*KIM et al.; "The Addressing Characteristics of an Alternating Current Plasma Display Panel Adopting a Ramping Reset Pulse"; August 2001.</p>			
<p>*LEE et al.; "Improvement of Addressing Time and its Dispersion in AC PDP"; December 2001.</p>			
<p>*LEE et al.; "Improvement of the Contrast Ratio and Reduction of the Reset Period by Current Controlled Ramp Waveform"; December 2001.</p>			
<p>*LEE et al.; "Improvement of the Luminance and Contrast Ratio by Current Controlled Ramp Waveform in AC PDP"; December 2001.</p>			
<p>*LEOU et al.; "Study of Discharge Characteristics of a Color PDP Cell Using Ramp Type Setup Voltage Pulse; June 2001.</p>			
<p>*OVERSLUIZEN et al.; "Characteristics of High-Xe Concentration 4 inch PDP"; December 2001.</p>			
<p>*PARK et al.; "A Modified Ramp Waveform to Reduce Rest Period in AC Plasma Display Panel"; May 2002.</p>			
<p>*PARK et al.; "A New Driving Waveform to Improve Dark Room Contrast in Ratio in AC Plasma Display Panel"; July 2002.</p>			
<p>*PARK et al.; "Improvement of Addressing Time and its Dispersion in AC Plasma Display Panel"; October 2001.</p>			
<p>*SAKITA et al.; "Analysis of Cell Operation at Address Period Using Wall Voltage Transfer Function in Three-Electrode Surface-Discharge AC PDPs"; December 2001.</p>			
<p>*SAKITA et al.; "High-speed Address Driving Waveform Analysis Using Wall Voltage Transfer Function for Three Terminals and VT Close Curve in Three-Electrode Surface-Discharge AC PDPs"; June 2001.</p>			
<p>*SAKITA et al.; "Ramp Setup Design Technique in Three-Electrode Surface-Discharge AC PDPs"; May 2002.</p>			
<p>*TAMAKA et al.; "A New Progressive Driving Scheme for a PDP with "CASTLE" Structure"; December 2001.</p>			
<p>*WEBER; "Color Plasma Displays"; May 2002.</p>			
<p>*YOON et al.; "Selective Charge-inversion Addressing Method for AC Plasma Display Panel"; April 2002.</p>			
EXAMINER	DATE CONSIDERED		

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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\* Please note that this reference is not currently available. However, it will be submitted upon availability.